



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET**CONFIRMATION NO. 5216**

Bib Data Sheet

SERIAL NUMBER 09/776,528	FILING DATE 02/04/2001 RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 15.32/5685
-----------------------------	---------------------------------------	--------------	------------------------	--------------------------------------

APPLICANTS

Tadashi Komiyama, Kofu-shi, JAPAN;
 Akitoshi Hara, Nagano-ken, JAPAN;
 Eiichi Sato, Okaya-shi, JAPAN;

** CONTINUING DATA ***** *NONE* *ns*

** FOREIGN APPLICATIONS ***** *verified* *ns*

JAPAN 2000-028366 02/04/2000
 JAPAN 2001-014025 01/23/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 03/14/2001

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 7
---	--	------------------------------	------------------------	-----------------------	----------------------------

Verified and Acknowledged *ns* Examiner's Signature *ns* Initials *ns*

ADDRESS
 Konrad, Raynes & Victor, LLP
 Suite 210
 315 South Beverly Drive
 Beverly Hills, CA
 90212

TITLE
 Methods for manufacturing semiconductor chips, methods for manufacturing semiconductor devices, semiconductor chips, semiconductor devices, connection substrates and electronic devices

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
------------	--	--